AMENDMENTS TO THE ABSTRACT

Replace the abstract with the following rewritten version:

"The invention relates to a ~~H~~hardware implemented filtering method ~~comprising the steps of~~ including establishing a representation DIS of the derivative of at least a part of a time-quantized input signal IS, and ~~–~~ establishing at least one sample of a time- and amplitude-quantized output signal OS by performing filtering on the basis of at least a part of a filter representation IFC1, IFC2, IFC3 and ~~said~~the representation DIS of the derivative of at least a part of ~~said~~the input signal IS. The invention further relates to a ~~H~~hardware implemented decimation method for decimating a time-quantized input signal IS ~~comprising the steps of~~ including dividing ~~said~~the time-quantized input signal IS into intervals, ~~——~~ for each of ~~said~~ the intervals establishing a sample of a time- and amplitude-quantized output signal OS according to the above mentioned filtering method. The invention further relates to a fast filtering means FFM implementing the above-mentioned methods."